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THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of: **NAKATA, Shunji et al.**

Group Art Unit: **2816**

Serial No.: **10/709,287**

Examiner: **C. F. COX**

Filed: **April 27, 2004**

P.T.O. Confirmation No.: 3286

For. **ADIABATIC CHARGING REGISTER CIRCUIT**

INFORMATION DISCLOSURE STATEMENT AND
STATEMENT PURSUANT TO 37 CFR 1.97(c)

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

February 23, 2005

Sir:

The attention of the Patent and Trademark Office is hereby directed to the documents listed on the attached Form PTO-1449. One copy of each of these documents is attached.

This Information Disclosure Statement is being submitted after issuance of a first official action on the merits and expiration of the three month period following the filing date or the entry in the national stage for the above-captioned application, but prior to issuance of either a final official action, a Notice of Allowance, or an action that otherwise closes prosecution in the application.

The undersigned hereby certifies

XX that each item of information contained in this statement was first cited in any communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this statement.

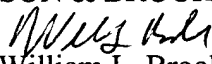
The above information is presented so that the Patent and Trademark Office may, in the first instance, determine any materiality thereof to the claimed invention. It is respectfully requested that

the information be expressly considered during the prosecution of this application, and that the references cited in the attached Form PTO-1449 be made of record therein and appear on the first page of any patent to issue therefrom.

The Commissioner is authorized to charge Deposit Account No. 01-2340 for any fee which is deemed by the Patent and Trademark Office to be required to effect consideration of this statement.

Respectfully submitted,

ARMSTRONG, KRATZ, QUINTOS,
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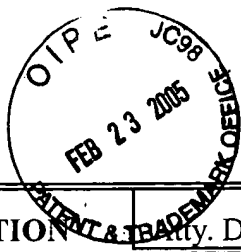
WLB/mla
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PATENT TRADEMARK OFFICE

Enclosures: PTO 1449
4 References
Copy of European Patent Office Communication for corresponding European Patent
Application 01410063 dated February 4, 2005



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| INFORMATION DISCLOSURE CITATION PTO-1449 | Patent No. 010704A | Serial No. 10/709,287 |
| | Applicant(s): NAKATA, Shunji et al. | |
| | Filing Date: April 27, 2004 | Group Art Unit: 2816 |

U.S. PATENT DOCUMENTS

| Examiner Initial | Document No. | Name | Date | Class | Subclass | Filing Date (If appropriate) |
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| _____ | AK | Kim et al., "A Low-Power Half-Swing Clocking Scheme for Flip-Flop with Complementary Gate and Source Drive", IEICE TRANS. ELECTRON., Vol. E82-C, No. 9, September 1999, pages 1777-1779. |
| _____ | AL | Copy of European Patent Office Communication with European Search Report for corresponding European Patent Application No. 01410063 dated February 4, 2005. |
| Examiner | | Date Considered |